

EL97M1666864S

Attorney Docket No.
AUS920030368US1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Boerstler et al.

Title: Clock Signal Selector Circuit With Reduced Probability of Erroneous Output Due to Metastability

Serial No.: To be assigned

Filed: Herewith

Group: To be assigned

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with the duty of disclosure set forth in 37 C.F.R. ' 1.56, Applicants wish to bring the items cited in the attached Form PTO/SB/08 to the attention of the Examiner. A copy of each item is enclosed for the convenience of the Examiner.

No representation is made, and no representation is intended, that more relevant material does not exist, or that the order of presentation of this material in any way reflects the relative pertinence. The references cited are not intended to constitute an admission of any kind. Specifically, this presentation is not an admission that the items listed in the attached Form PTO/SB/08 are properly citable against the above-identified application as prior art.

Attorney Docket No.
AUS920030368US1

PATENT

Applicants consider the invention to be distinguishable over the references cited.

As this Information Disclosure Statement is being filed before the issuance of the first office action, no fee is enclosed.

Respectfully submitted,

Date: 8-20-03


~~Robert M Carwell~~ Mark McBurrey
Registration No. 28,499 33,114
Intellectual Property Law Dept.
IBM Corporation
11400 Burnet Road
Austin, Texas 78758
Telephone: (512) 823-1017 1003

Correspondence Address:
Gregory W. Carr, Reg. No. 31,093
CARR LLP
Intellectual Property Law
670 Founders Square
900 Jackson Street
Dallas, Texas 75202
(214) 760-3000
(214) 760-3003 (fax)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		ATTORNEY DOCKET NO.: AUS920030368US1	SERIAL NO.: To be assigned
		APPLICANT: Boerstler et al.	EXAMINER: To be assigned
		FILING DATE: Herewith	GROUP: To be assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Figures Appear
		Number	Kind Code ²			
	AA	4,999,528	B1	Keech		
	AB	5,418,407	B1	Frenkil		
	AC	5,510,732	B1	Sandhu		
	AD	5,754,070	B1	Baumann et al.		
	AE	5,793,227	B1	Goldrian		
	AF	5,867,695	B1	Amini et al.		
	AG	5,999,029	B1	Nguyen et al.		
	AH	6,072,346	B1	Ghahremani		
	AI	6,384,619	B1	Kim et al.		
	AJ	6,429,692	B1	Chan et al.		

FOREIGN PATENT DOCUMENTS

	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵				
	BA							
	BB							
	BC							

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

	CA		RABAЕY, Jan; <i>Digital Integrated Circuits: A Design Perspective</i> ; pp. 533-537; Prentice Hall (1996).
	CB		
	CC		

EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issue the document, by the two-letter code (WIPO Standard ST. 3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Place a check mark here if English language Translation is attached.